

## CLAIMS

What is claimed is:

- 1        1.        An integrated circuit (IC) comprising:  
2                at least one circuit path sensitive to floating body effects;  
3        **TP**        a body effect ~~monitor~~ monitoring circuit activity for said at least one circuit path  
4                and, selectively providing an indication of said body effect charging responsive to  
5                monitored circuit inactivity;  
6                a pulse generator generating a neutralization pulse responsive to an indication of  
7                said floating body effects; and  
8                a neutralization pulse distribution circuit selectively providing said neutralization  
9                pulse to blocks in said circuit path.
- 1        2.        An IC as in claim 1, wherein said circuit path is a logic signal path and said  
2                blocks are logic blocks.
- 1        3.        An IC as in claim 2, wherein said neutralization pulse distribution circuit is a shift  
2                register comprising a plurality of shift register latches.
- 1        4.        An IC as in claim 3, wherein said neutralization pulse is sequentially shifted  
2                through said plurality of shift register latches.
- 1        5.        An IC as in claim 3, said IC further comprising:  
2                a test circuit, selectively providing test data for testing said blocks; and  
3                a multiplexor selectively passing data from said test circuit and neutralization  
4                pulses from said pulse generator to said shift register.

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